



Computational Complexity Theory

Lecture 17: Class NC and AC

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Recap: Lesson learnt from Cook-Levin

- Locality of computation implies that an algorithm A working on inputs of some fixed length n and running in time $T(n)$ can be viewed as a Boolean circuit ϕ of size $O(T(n)^2)$ s.t. $A(x) = \phi(x)$ for every $x \in \{0,1\}^n$.
- On the other hand, a circuit on inputs of length n and of size S can be viewed as an algorithm working on length n inputs and running in time S .
- To rule the existence of a sequence of algorithms – one for each input length – we need to rule out the existence of a sequence of (i.e., a family of) circuits.

Recap: Boolean circuits

- A Boolean circuit is a directed acyclic graph whose nodes/gates are labelled as follows:
 - A node with in-degree zero is labelled by an input variable, and it outputs the value of the variable.
 - Any other node is labelled by one of the three operations \wedge , \vee , \neg , and it outputs the value of the operation on its input.

Nodes with out-degree zero are the output gates.

- **Size** of circuit is the no. of edges in it. **Depth** is the length of the longest path from an i/p to o/p node.

Recap: Class P/poly

- Let $T: \mathbb{N} \rightarrow \mathbb{N}$ be some function.
- **Definition:** A $T(n)$ -size circuit family is a set of circuits $\{C_n\}_{n \in \mathbb{N}}$ such that C_n has n inputs and $|C_n| \leq T(n)$.
- **Definition:** A language L is in $\text{SIZE}(T(n))$ if there's a $T(n)$ -size circuit family $\{C_n\}_{n \in \mathbb{N}}$ such that
$$x \in L \iff C_n(x) = 1, \text{ where } n = |x|.$$
- **Definition:** Class $\text{P/poly} = \bigcup_{c \geq 1} \text{SIZE}(n^c)$.

Recap: Class P/poly

- **Observation:** $P \subseteq P/poly$.
- **Proof.** If $L \in P$, then there's a n^c -time TM that decides L for some constant c . By Cook-Levin, there's a $O(n^{2c})$ -size circuit family $\{C_n\}_{n \in \mathbb{N}}$ such that
$$x \in L \iff C_n(x) = 1, \text{ where } n = |x|.$$
(Note: C_n is $poly(n)$ -time computable from 1^n .)
- Is $P = P/poly$? **No!** $P/poly$ contains undecidable languages.

Recap: Karp-Lipton theorem

- **Theorem** (*Karp & Lipton 1982*). If $NP \subsetneq P/poly$ then $PH = \Sigma_2$.
- If we can show $NP \not\subseteq P/poly$ assuming $P \neq NP$, then
$$NP \not\subseteq P/poly \iff P \neq NP.$$
- Karp-Lipton theorem shows $NP \not\subseteq P/poly$ assuming the stronger statement $PH \neq \Sigma_2$.

Recap: Functions outside P/poly

- Are there Boolean functions (i.e., languages) outside P/poly? **Yes!** There are many. Let $\exp(m) = 2^m$.
- **Theorem.** $1 - \exp(-2^{n-1})$ fraction of Boolean functions on n variables **do not** have circuits of size $2^n/(22n)$.
- Proof. Follows from a counting argument.

Recap: Functions outside P/poly

- Are there Boolean functions (i.e., languages) outside P/poly? **Yes!** There are many.
- Is one out of so many functions outside P/poly in NP? We don't know even after ~40 yrs of research!
- **Theorem.** (Iwama, Lachish, Morizumi & Raz 2002)
There is a language $L \in \text{NP}$ such that any circuit C_n that decides $L \cap \{0,1\}^n$ requires $5n - o(n)$ many \wedge and \vee gates.

↑
Results of this kind are known as
circuit lower bound.

Lower bounds for restricted circuits

- Nevertheless, the clean combinatorial structure of a circuit has been used to prove lower bounds for some natural classes of circuits.
- The proofs of these lower bounds introduced and developed some highly interesting techniques.

Lower bounds for restricted circuits

- Nevertheless, the clean combinatorial structure of a circuit has been used to prove lower bounds for some natural classes of circuits.
- The proofs of these lower bounds introduced and developed some highly interesting techniques.
- **Fact.** $\text{PARITY}(x_1, x_2, \dots, x_n)$ can be computed by a circuit of size $O(n)$ and a formula of size $O(n^2)$.

Homework

Lower bound for Boolean formulas

- Nevertheless, the clean combinatorial structure of a circuit has been used to prove lower bounds for some natural classes of circuits.
- The proofs of these lower bounds introduced and developed some highly interesting techniques.
- **Theorem.** (*Khrapchenko 1971*) Any formula computing **PARITY**(x_1, x_2, \dots, x_n) has size $\Omega(n^2)$.

Lower bound for Boolean formulas

- Nevertheless, the clean combinatorial structure of a circuit has been used to prove lower bounds for some natural classes of circuits.
- The proofs of these lower bounds introduced and developed some highly interesting techniques.
- **Theorem.** (*Andreev 1987, Hastad 1998*) There's a **f** that can be computed by a **$O(n)$** -size circuit such that any formula computing **f** has size **$\Omega(n^{3-o(1)})$** .

Technique: Shrinkage of formulas under random restrictions (*Subbotovskaya 1961*).

Lower bound for Boolean formulas

- Nevertheless, the clean combinatorial structure of a circuit has been used to prove lower bounds for some natural classes of circuits.
- The proofs of these lower bounds introduced and developed some highly interesting techniques.
- **Conjecture.** (Circuits more powerful than formulas)
There's a f that can be computed by a $O(n)$ -size circuit such that any formula computing f has size $n^{\omega(1)}$.

An interesting approach was given by
Karchmer, Raz & Wigderson (1995).

LB for AC^0 and monotone circuits

- Nevertheless, the clean combinatorial structure of a circuit has been used to prove lower bounds for some natural classes of circuits.
- The proofs of these lower bounds introduced and developed some highly interesting techniques.
- We'll discuss a **super-polynomial** lower bound for constant depth circuits later.

Non-uniform size hierarchy

- **Shanon's result.** There's a constant $c \geq 1$ such that every Boolean function in n variables has a circuit of size at most $c \cdot (2^n/n)$.
- **Theorem.** There's a constant $d \geq 1$ s.t. if $T_1: \mathbb{N} \rightarrow \mathbb{N}$ & $T_2: \mathbb{N} \rightarrow \mathbb{N}$ and $T_1(n) \leq d^{-1} \cdot T_2(n) \leq T_2(n) \leq c \cdot (2^n/n)$ then
$$\text{SIZE}(T_1(n)) \subsetneq \text{SIZE}(T_2(n)).$$

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$$\text{SIZE}(T_1(n)) \subsetneq \text{SIZE}(T_2(n)).$$
- **Proof.** Uses Shanon's result and a counting argument.
(Homework)

Class NC^i and AC^i

Class NC

- **NC** stands for Nick's Class – named after Nick Pippenger.
- **Definition.** For $i \in \mathbb{N}$, a language L is in NC^i if there is a polynomial function $q(\cdot)$ and a constant c s.t. L is decided by a $q(n)$ -size circuit family $\{C_n\}_{n \in \mathbb{N}}$, where depth of C_n is at most $c \cdot (\log n)^i$ for every $n \in \mathbb{N}$.
- **Definition.** $NC = \bigcup_{i \in \mathbb{N}} NC^i$.

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- **Definition.** $NC = \bigcup_{i \in \mathbb{N}} NC^i$.
- **Homework:** **PARITY** is in NC^1 .

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- **Definition.** $NC = \bigcup_{i \in \mathbb{N}} NC^i$.
- $NC^1 = \text{poly}(n)$ -size Boolean formulas. (*Assignment*)

Class NC

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- Further, L is in log-space uniform NC^i if C_n is implicitly log-space computable from 1^n .

Note: Sometimes NC^i is defined as log-space uniform NC^i .

Class NC

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log-space uniform $NC \subseteq P$.

NC \equiv Efficient parallel computation

- **Definition.** A language L can be decided efficiently in parallel if there's a polynomial function $q(\cdot)$ and constants c & i s.t. $L \cap \{0,1\}^n$ can be decided using $q(n)$ many processors in $c \cdot (\log n)^i$ time.

NC \equiv Efficient parallel computation

- **Definition.** A language L can be decided efficiently in parallel if there's a polynomial function $q(.)$ and constants c & i s.t. $L \cap \{0,1\}^n$ can be decided using $q(n)$ many processors in $c \cdot (\log n)^i$ time.
- Model: **PRAM** (has a central shared memory)
 - A processor can “deliver” a message to any other processor in $O(\log n)$ time.
 - A processor has $O(\log n)$ bits of memory and performs a poly-time computation at every step.
 - Processor computation steps are synchronized.


NC \equiv Efficient parallel computation

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- **Observation.** A language L is in NC if and only if L can be decided efficiently in parallel.
- **Proof.** Almost immediate from the assumptions on the parallel computation model.

Class AC

- **Definition.** For $i \in \mathbb{N} \cup \{0\}$, a language L is in AC^i if there is a polynomial function $q(\cdot)$ and a constant c s.t. L is decided by a $q(n)$ -size **unbounded fan-in** circuit family $\{C_n\}_{n \in \mathbb{N}}$, where depth of C_n is at most $c \cdot (\log n)^i$ for every $n \in \mathbb{N}$.
- **Definition.** $AC = \bigcup_{i \geq 0} AC^i$. (stands for *Alternating Class*)

Class AC

- **Definition.** For $i \in \mathbb{N} \cup \{0\}$, a language L is in AC^i if there is a polynomial function $q(\cdot)$ and a constant c s.t. L is decided by a $q(n)$ -size **unbounded fan-in** circuit family $\{C_n\}_{n \in \mathbb{N}}$, where depth of C_n is at most $c \cdot (\log n)^i$ for every $n \in \mathbb{N}$.
- **Definition.** $AC = \bigcup_{i \geq 0} AC^i$.
- **Observation.** $AC^i \subseteq NC^{i+1} \subseteq AC^{i+1}$ for all $i \geq 0$.


Replace an unbounded fan-in gate by a binary tree of bounded fan-in gates.

Class AC

- **Definition.** For $i \in \mathbb{N} \cup \{0\}$, a language L is in AC^i if there is a polynomial function $q(\cdot)$ and a constant c s.t. L is decided by a $q(n)$ -size **unbounded fan-in** circuit family $\{C_n\}_{n \in \mathbb{N}}$, where depth of C_n is at most $c \cdot (\log n)^i$ for every $n \in \mathbb{N}$.
- **Definition.** $AC = \bigcup_{i \geq 0} AC^i$.
- **Observation.** $NC = AC$.

Class AC

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- **Definition.** $AC = \bigcup_{i \geq 0} AC^i$.
- In the next lecture, we'll show that **PARITY** is not in AC^0 , i.e., $AC^0 \subsetneq NC^1$.

Class AC

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log-space uniform $AC \subseteq P$.